



FACULTY PROFILE	
Name	Dr. D. Sreedevi
Date of Birth	10.03.1972
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Contact	9989590895
Education Qualifications	<p>M.Sc., M.Phil., Ph.D. in Electronics</p> <ul style="list-style-type: none">• Ph.D. in Electronics from Andhra University, Visakhapatnam in December 2021.• M.Phil. in Electronics with Distinction from Andhra University Visakhapatnam in 2006.• M.Sc. (Electronics) with Distinction from Acharya Nagarjuna University during 1992-94.• B.Sc. (Electronics) with First class from Andhra University during 1988-91.
Additional Certifications	<ul style="list-style-type: none">• 4 months industry driven VLSI Training Program from 26th March to 31st July 1999 in Qual Core Logic Pvt Ltd.• 12 weeks NPTEL course on Switching circuits and logic design in April 2024.• 12 weeks NPTEL course on Computer organization and architecture in April 2019.• 1 year P.G. Diploma in Computer Science from University of Hyderabad

<p>Teaching Experience</p>	<p>31 Years 6 Months</p> <ul style="list-style-type: none"> • Working as a Senior Assistant Professor in the Department of Basic Sciences & Humanities in Engineering and Technology Program 2025 till date. • Worked as a Senior Assistant Professor and HOD in Department of Electronics of GVPCDPGC (A) from 2009-2025. • Worked as an Assistant professor for 10 years in Department of Electronics of Pydah Degree College, Visakhapatnam from 1999-2009. • Worked as a Lecturer for 5 years in Department of Electronics of S.L.G. Degree College, Vizianagaram from 1994-1999.
<p>Area of Specialization</p>	<ul style="list-style-type: none"> • SODAR and ML
<p>Membership</p>	<ul style="list-style-type: none"> • Professional member of Institute for Educational research and Publication (IFERP)
<p>Books published</p>	<ul style="list-style-type: none"> • Authored two chapters for a text book of B.Sc. Electronics published by TELUGU ACADEMY.
<p>Patents</p>	<ul style="list-style-type: none"> • Filed and published a patent on “Analytics of Big Data Based on Machine Learning and Data Science for Patients suffering from Lung Cancer” in November 2022. • Filed and published a patent on “Smart IOT irrigation system with real time growth monitoring for efficient water use” in November 2024.

<p>No. of FDPs/workshops/ seminars/ training programs attended</p>	<p>Total: 59</p> <ul style="list-style-type: none"> • FDPs: 16 • Short term Trainings: 04 • National/International Workshops: 24 • National/International Seminars: 15
<p>Professional Roles in Examination and Administration</p>	<ul style="list-style-type: none"> • Nominated as member of BOS by the Vice Chancellor of Andhra University in Electronics (UG) for a period of 3 years. • Appointed as Coordinator for “Quantum Innovation Center” by the institution. • Acting as a member of IQAC cell. • Acted as spot chief for the Board of Electronics for Andhra University examinations. • Acted as paper setter and examiner for Sri Krishna University, Tamilnadu, Andhra University, Sri Krishna devaraya University and Nagarjuna University. • Acted as BOS Chair Person for BOS for Electronics in GVPCDPGC.
<p>Publications & Conferences</p>	<ul style="list-style-type: none"> • D.Sreedevi, N.V.Ramanamurty, DSSN Raju, Hareesh babu.M, M.P. Rao, “PC based real time data processing system for Doppler SODAR applications” , IJCSE Journal, Volume-5, issue-2, pp 160-167, Feb 2017, E-ISSN: 2347-2693. • D.Sreedevi, Prof.K.Samatha, Prof.M.P.Rao, A review on typical and modern brain MRI image segment methods and challenges, IJCERT journal, Volume 6, pp 322-329, May 2019, ISSN:2349-7084. • D.Sreedevi, Prof.K.Samatha, Prof.M.P.Rao, Performance of MRI brain tumor classification using neural network classifiers, IJARET, Vol-11, issue-9, ,pp.1043-1059, Sep 2020, ISSN :0976-6480.

Publications & Conferences

- NP Lavanya Kumari, Dr.D.Sreedevi, Prof.K.Samatha, “A real time audio signal De-noising using FIR filter with deep intelligence models” Neuro Quantology, Vol-20, Issue-6,pp 7866-7875, , June 2022 ISSN :1303-5150.
- Published a paper “Automatic Phizog based attendance monitoring system” in an International Research Journal on Advanced Engineering Hub (IRJAEH) with ISSN No 2584-2137 in Vol 2, in July 2024.
- Presented a paper in Two-day National seminar on “Emerging Trends in Material Science & Technology” organized by Andhra University on 9th & 10th March 2019.
- Presented a paper on “Revolutionizing Attendance Management with Phyzog Technology” in an International Conference on Recent Trends in Science Technology and Management (ICRTSTM) organized by BVCOEW, Pune, Maharashtra on June 28th & 29th, 2024.

Courses taught at Under Graduate and Post Graduate Level

- Computer Organization
- Circuit Theory
- Electronic Devices & Circuits
- Communication systems
- Digital Logic Design & Computer Architecture
- Operational Amplifiers
- Analog & Digital IC Applications
- Electronic circuits & PCB designing
- Microprocessors
- Microcontrollers
- Embedded Systems
- Industrial Electronics
- Instrumentation
- Bio Medical Instrumentation
- Consumer Electronics

Achievements

- Got university 1st rank in AU Research Common Entrance Test (AURCET).
- Received Best teacher award from GVPCDPGC twice for the academic years 2018 and 2023.
- Felicitated as best teacher by the students of ICFAI.
- Organized an Inter Collegiate Electronics Exhibition “ELECTRONICS EXPO” for three days in November 2003 in Pydah Degree College where 500 gadgets were displayed by various colleges of Andhra Pradesh and attracted 10,000 visitors.

Contact Details

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