

# G SOWJANYA LAKSHMI

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## Objective:

Seeking quality environment where my knowledge and vast experience can be shared and enriched looking for an opportunity where I can contribute in the growth of the organization and improve my technical skills with latest technological developments.

## Educational Qualifications:

- M.Tech VLSI-System Design, 2015, Avanthi Engineering College, 82%, JNTUK, AP.
- B.Tech ECE, 2013, Avanthi's Sri Gnaneswari Engineering College, 73.5%, JNTUK, AP.
- Intermediate M.P.C, 2009, Sri Chaitanya Junior College, 82.7%, Visakhapatnam, AP.
- S.S.C, 2007, Viswabharathi Vidya Nikethan, 81.5 %, Visakhapatnam, AP.

## Teaching Experience:

- I have been working in Avanthi Institute of Engineering & Technology as Asst. Professor, from June 2018 to till date.
- I worked in Simhadri College of Engineering as Asst. Professor, from Jan 2016 to Dec 2016.

## Academic Projects:

### M. Tech Project

- Title: Implementation of Speed optimized CSLA architecture using D-Latch Approach
- Environment : Xilinx ISE 12.3i

### B. Tech Project

- Title: AN ART OF SMART MUSEUM
- Environment : Keil Software. (Keilµvision4)

## Academic Achievements:

- Got merit certificate in Online Capacity Building Programme on “**INTERNET OF THINGS (IoT)**” Organized by Department of Electronics and Communication Engineering of Annamacharya Institute of Technology and Sciences, Tirupati, from 21st to 25<sup>th</sup> May 2020.
- Got Topper in **M. Tech Programme** - VLSI design.
- My **U.G project-AN ART OF SMART MUSEUM** elected as best project in all.
- Got First prize in the event **PRACSTANTIA'2K11** A National level student technical symposium organized by Kaushik College of Engineering (**Topic: Pill Camera**).

## Publications:

- G.SOUJANYA, M.KEDARESWARA RAO “**Implementation of Speed Optimized CSLA Architecture using D-Latch Approach**” International Journal of VLSI System Design and Communication Systems, Volume.03, IssueNo.08, October-2015, Pages: 1169-1172.

## Workshops:

- Participated in “one day workshop on eSim, a First Course in the IoT Series for Teachers” held at **Gayatri Vidya Parishad College of Engineering(Autonomous)** collaboration with **IIT Bombay** on 21<sup>st</sup> September 2019.
- Participated in Online Capacity Building Programme on “**INTERNET OF THINGS (IoT)**” Organized by Department of Electronics and Communication Engineering of **Annamacharya Institute of Technology and Sciences, Tirupati**, from 21<sup>st</sup> to 25<sup>th</sup> May 2020.
- Participated in a workshop on “**VLSI Circuits Analog and Digital design perspective**” conducted by Department of Electronics and Communication Engineering in association with “**AMS Semiconductors & HCL Technologies**” from 16<sup>th</sup>-17<sup>th</sup> of May 2020.

## Subjects Taught:

- RADAR Engineering
- Microprocessors and Microcontrollers
- Linear and IC Applications
- EMI

## Projects Guided:

- Smart Medicine Box.
- Pick and place ROBOT.

## Strengths:

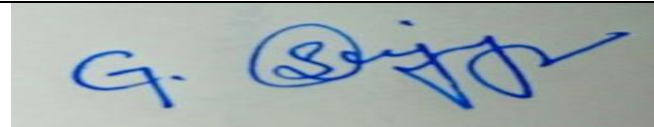
- Interest in learning and developing new things.
- A quick study, adapt easily to new computer environment and technologies.
- Proven strength in problem solving, co-ordination and Analysis.

## Personal Details:

- Date of Birth : 15-4-1992.
- Father’s name : G.L.Naidu.
- Marital Status : Married
- Languages known : Telugu, English and Hindi.
- Nationality : Indian.
- Address : Door no: 54-10-2/1,  
Dr. NGR’S Lotus Plaza,  
Isukaththa,  
Visakhapatnam-17.

Declaration:

I hereby declare that the information furnished above is true to the best of my knowledge.

Place : Visakhapatnam	
Date :	
(G Sowjanya Lakshmi)	